

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**  
( Not for submission under 37 CFR 1.99)

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Filing Date	2006-05-18
First Named Inventor	Murthi Nanja
Art Unit	2122
Examiner Name	Not Yet Assgined
Attorney Docket Number	20002/17226

<i>EP</i>	1	Submitted herewith is a copy of the International Preliminary Report on Patentability for International application no. PCT/US2004/029083 dated March 23, 2006.	<input type="checkbox"/>
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<b>EXAMINER SIGNATURE</b>			
Examiner Signature	<i>Chayda Phee</i>	Date Considered	10/15/06
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Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office		
INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)		Atty. Docket No. Intel/17226	Serial No. 10/659,457
		Applicant Nanja et al.	
		Filing Date 09-10-03	Group Art Unit 2122

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)		
✓	C01	<i>International Search Report for International Patent Application Serial Number PCT/US2004/029083; mailed February 7, 2005; 4 pages.</i>
	C02	<i>Written Opinion of the International Searching Authority for International Patent Application Serial Number PCT/US2004/029083; mailed February 7, 2005; 7 pages.</i>
	C03	<i>Krishnaswamy et al., "Profile Guided Selection of ARM and Thumb Instructions," Sigplan Notices ACM USA, Vol. 37, No. 7, July 2002; pages 56-64.</i>
	C04	<i>Krishnaswamy et al., "Mixed-Width Instructions Sets," Communications of the Association for Computing Machinery, Association for Computing Machinery, New York, Vol. 46, No. 8, August 2003; pages 47-52.</i>
	C05	<i>Fischer et al., "Optimal Mixing of 16 bit and 32-bit computer codes by means of "Codebalance",", Electronik Weka-Fachzeitschriften Germany, Vol. 48, No. 19, September 21, 1999; pages 96-99.</i>
	C06	<i>Halambi et al., "An Efficient Compiler Technique for Code Size Reduction Using Reduced Bit-Width ISAs", Proceedings 2002 Design, Automations and Test in Europe Conference and Exhibition IEEE Computer Society, Los Alamitos, California, March 2002; pages 402-408.</i>
↓	C07	<i>Delsart et al., "JCOD: a Lightweight Modular Compilation Technology for Embedded Java", Lecture Notes in Computer Science, Springer Verlag, New York, New York, Vol. 2491, October 2002; pages 197-212.</i>
↓	C08	<i>Clark D: "Mobile Processors Begin to Grow Up", Computer IEEE Computer Society USA, Vol. 35, No. 3, March 2002; pages 22-25.</i>

EXAMINER <i>John Haupler</i>	DATE CONSIDERED <i>10/15/06</i>
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